Machine Learning Bootcap

CSCE 4013/5012 Domain Specific Architectures Professor David Andrews

Lecture materials drawn from the following paper:

Hennessy and Patterson Computer Architecture: A Quantitative Approach, 6 ed, Morgan Kaufmann



Guidelines for DSAs

Guideline	TPU	Catapult	Crest	Pixel Visual Core
Design target	Data center ASIC	Data center FPGA	Data center ASIC	PMD ASIC/SOC IP
1. Dedicated memories	24 MiB Unified Buffer, 4 MiB Accumulators	Varies	N.A.	Per core: 128 KiB line buffer, 64 KiB P.E. memory
2. Larger arithmetic unit	65,536 Multiply- accumulators	Varies	N.A.	Per core: 256 Multiply- accumulators (512 ALUs)
3. Easy parallelism	Single-threaded, SIMD, in-order	SIMD, MISD	N.A.	MPMD, SIMD, VLIW
 Smaller data size 	8-Bit, 16-bit integer	8-Bit, 16-bit integer 32-bit Fl. Pt.	21-bit Fl. Pt.	8-bit, 16-bit, 32-bit integer
5. Domain- specific lang.	TensorFlow	Verilog	TensorFlow	Halide/TensorFlow



Hennessy and Patterson Computer Architecture: A Quantitative Approach

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Deep Neural Networks (DNNs)

- Inspired by neuron of the brain
- Computes non-linear "activation" function of weighted sum of input values
- Neurons arranged in layers
- 3 Categories
 - Multilayer Perceptron (MLP)
 - Recurrent Neural Networks (RNNs)
 - LSTMs, Transformers
 - Convolutional Neural Networks (CNNs),

Name	DNN layers	Weights	Operations/Weight 200	
MLP0	5	20M		
MLP1	4	5M	168	
LSTM0	58	52M	64	
LSTM1	56	34M	1 96	
CNN0	16	8M	2888	
CNNI	89	100M	1750	

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Deep Neural Networks

Most practitioners choose existing design

- Topology
- Data type

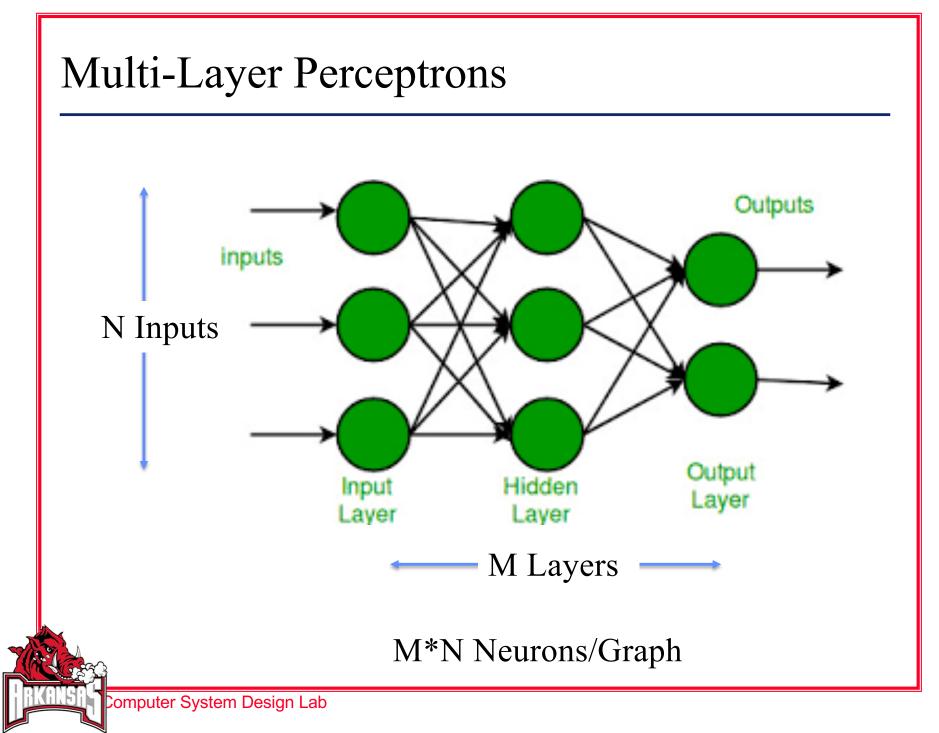
Training (learning):

- Calculate weights using backpropagation algorithm
- Supervised learning: stochastic graduate descent

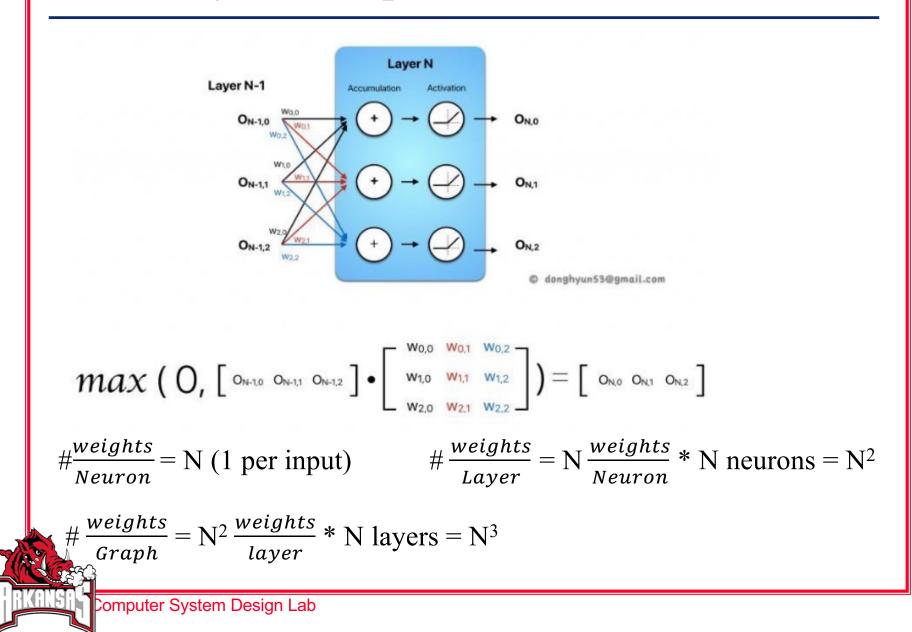
Type of data	Problem area	Size of benchmark's training set	DNN architecture	Hardware	Training time
text [1]	Word prediction (word2vec)	100 billion words (Wikipedia)	2-layer skip gram	1 NVIDIA Titan X GPU	6.2 hours
audio [2]	Speech recognition	2000 hours (Fisher Corpus)	11-layer RNN	1 NVIDIA K1200 GPU	3.5 days
images [3]	Image classification	1 million images (ImageNet)	22-layer CNN	1 NVIDIA K20 GPU	3 weeks
video [4]	activity recognition	1 million videos (Sports-1M)	8-layer CNN	10 NVIDIA GPUs	1 month



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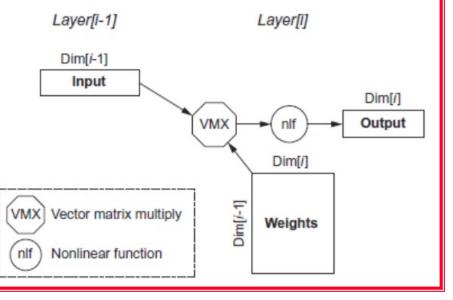
Multi-Layer Perceptrons



Multi-Layer Perceptrons

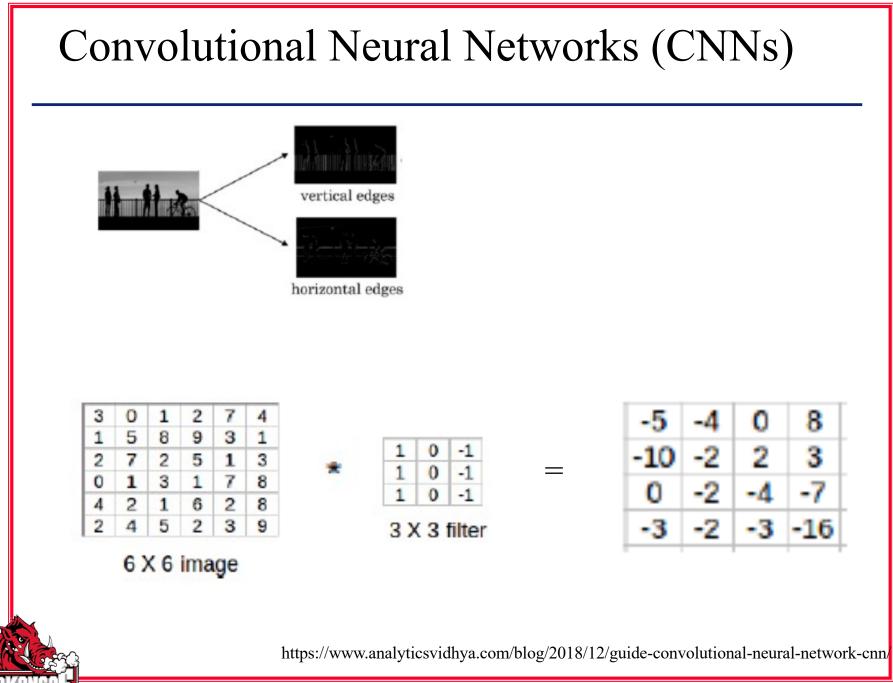
Parameters/Layer:

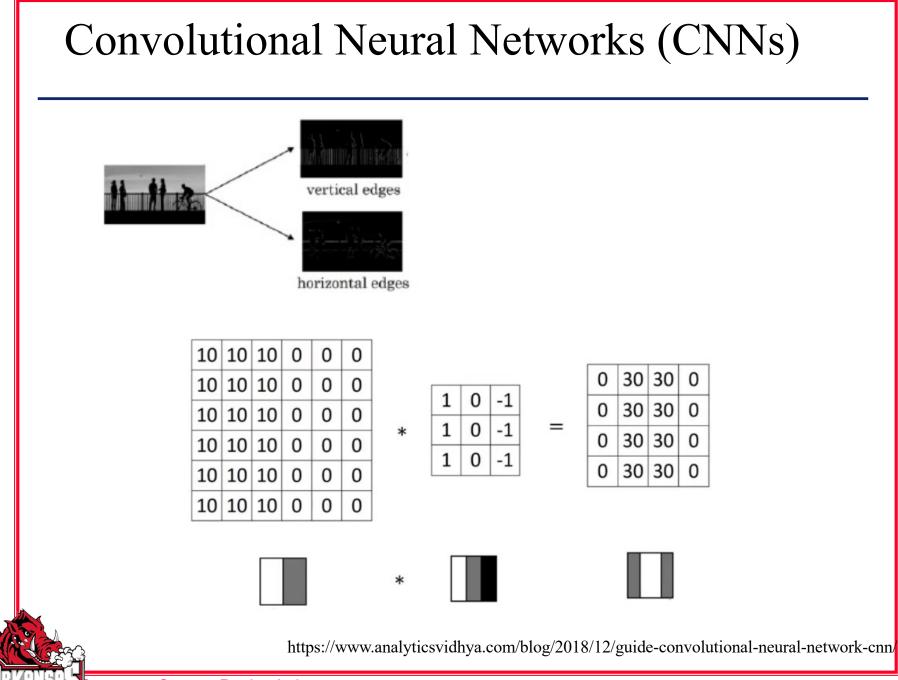
- Dim[i]: number of neurons
- Dim[i-1]: dimension of input vector
- Number of weights: Dim[i-1] x Dim[i]
- Operations: 2 x Dim[i-1] x Dim[i]
- Operations/weight: 2



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Padding.....

Padding

We have seen that convolving an input of 6×6 dimension with a 3×3 filter results in 4×4 output. We can generalize it and say that if the input is n X n and the filter size is f X f, then the output size will be (n-f+1) X (n-f+1):

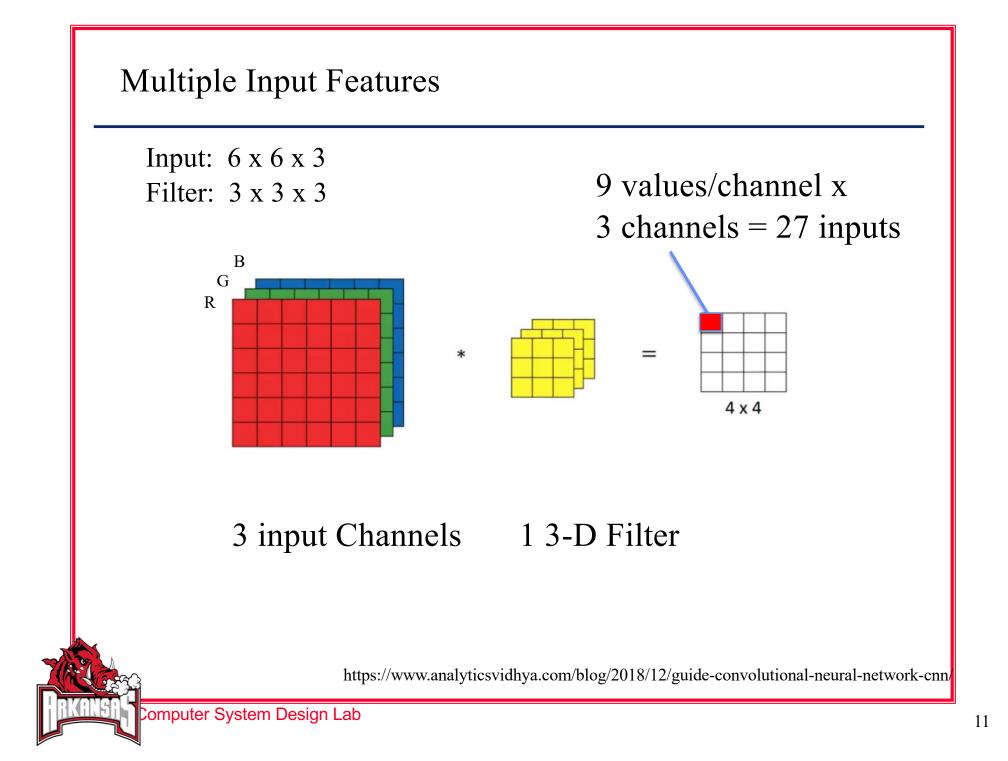
- Input: nXn
- Filter size: f X f
- Output: (n-f+1) X (n-f+1)

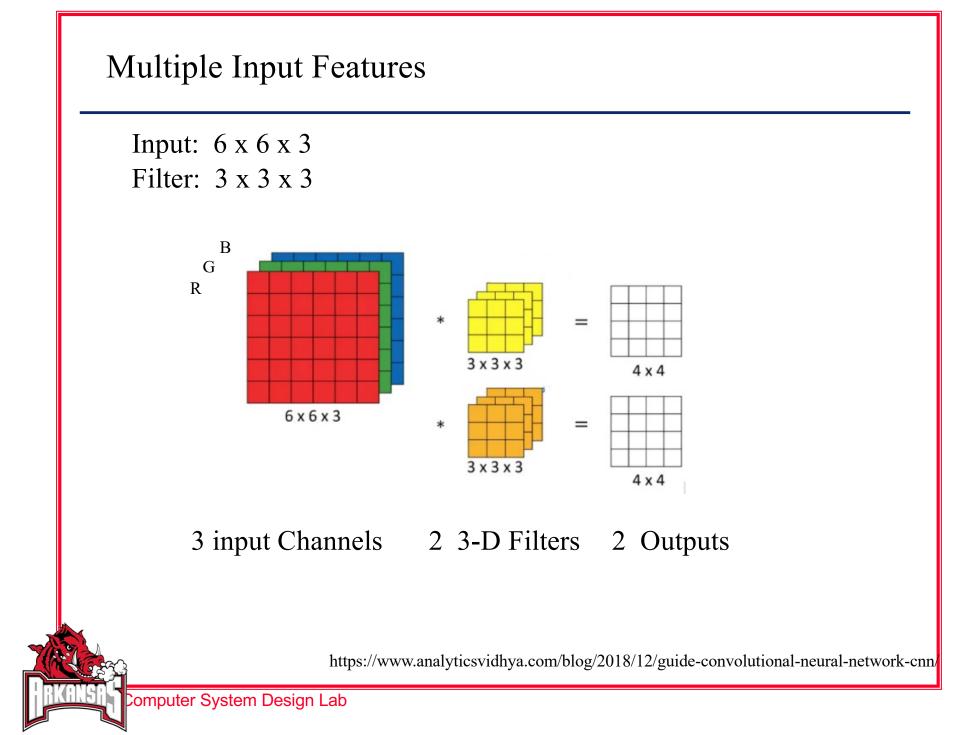
To overcome these issues, we can pad the image with an additional border, i.e., we add one pixel all around the edges. This means that the input will be an 8 X 8 matrix (instead of a 6 X 6 matrix). Applying convolution of 3 X 3 on it will result in a 6 X 6 matrix which is the original shape of the image. This is where padding comes to the fore:

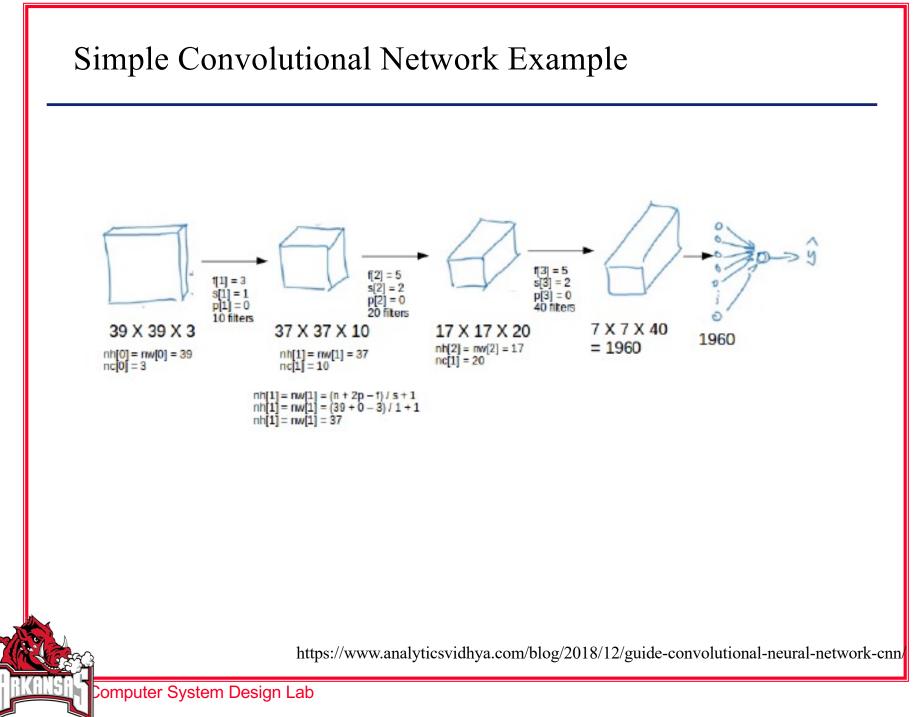
- Input: n X n
- Padding: p
- Filter size: f X f
- Output: (n+2p-f+1) X (n+2p-f+1)

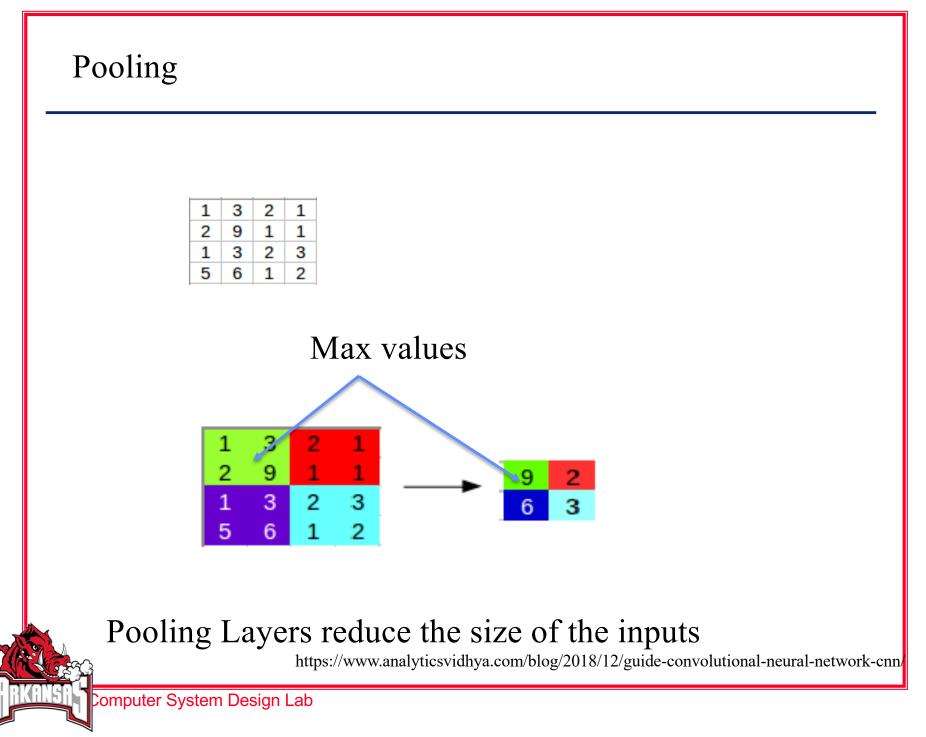
https://www.analyticsvidhya.com/blog/2018/12/guide-convolutional-neural-network-cnn/

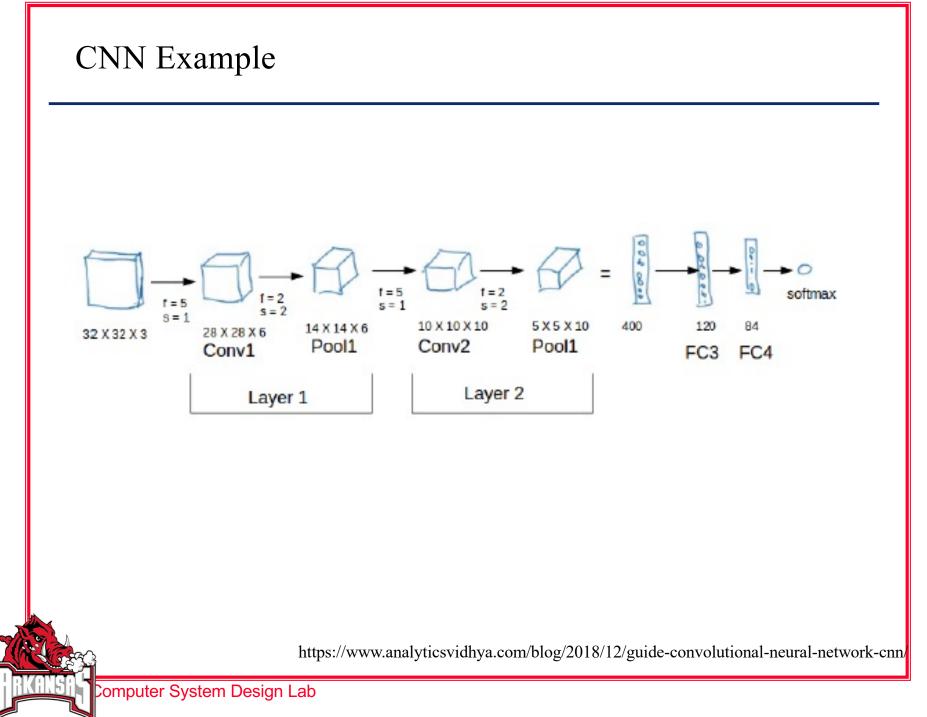
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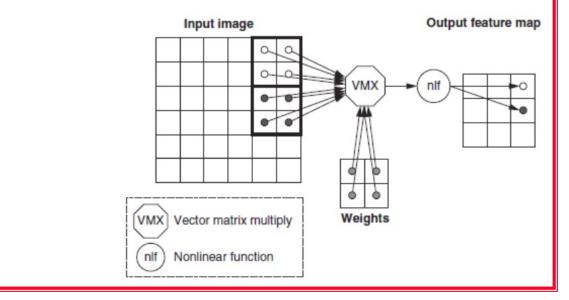




Convolutional Neural Network

Each layer raises the level of abstraction

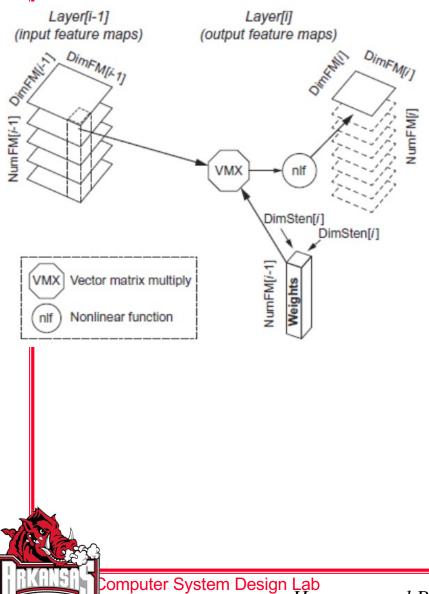
- First layer recognizes horizontal and vertical lines
- Second layer recognizes corners
- Third layer recognizes shapes
- Fourth layer recognizes features, such as ears of a dog
- Higher layers recognizes different breeds of dogs



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Convolutional Neural Network



Parameters:

- DimFM[i-1]: Dimension of the (square) input Feature Map
- DimFM[i]: Dimension of the (square) output • Feature Map
- DimSten[i]: Dimension of the (square) stencil ٠
- NumFM[i-1]: Number of input Feature Maps ٠
- NumFM[i]: Number of output Feature Maps ٠
- Number of neurons: NumFM[i] x DimFM[i]²
- Number of weights per output Feature Map: NumFM[i-1] x DimSten[i]²
- Total number of weights per layer: NumFM[i] ٠ x Number of weights per output Feature Map
- Number of operations per output Feature Map: 2 x DimFM[i]² x Number of weights per output Feature Map
- Total number of operations per layer: NumFM[i] × Number of operations per output Feature Map = $2 \times \text{DimFM}[i]^2 \times \text{NumFM}[i] \times$ Number of weights per output Feature Map = 2 x DimFM[i]² x Total number of weights per layer
- Operations/Weight: 2 x DimFM[i]²

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